

WHAT IS CLAIMED IS:

1. A high frequency switch comprising:

an input terminal;

5 an output terminal;

a first diode provided in a signal line, which connects said input terminal to said output terminal, said first diode having an input end and an output end;

10 a second diode having one end, which is connected to the input end side of the first diode;

a first resistor provided in a terminated line, which is connected to another end of the second diode;

a third diode having one end, which is connected to the output end side of the first diode; and

15 a second resistor provided in a terminated line, which is connected to another end of the third diode.

2. A high frequency switch according to claim 1, further comprising capacitors connected to the ends of the first to 20 third diodes in order to make ON-OFF control of the diodes and DC bias circuits connected thereto in order to apply a DC bias.

3. A high frequency switch comprising:

25 a single input terminal;

a plurality of output terminals;

a plurality of first diodes provided in respective signal lines, which connect said input terminal to said plurality of output terminals, each of said plurality of first diodes having an input end and an output end;

5 a second diode having one end, which is connected to input end sides of the plurality of first diodes;

 a first resistor provided in a terminated line, which is connected to another end of the second diode;

 a plurality of third diodes having ends, which are
10 respectively connected to the outputs of the plurality of first diodes; and

 a plurality of second resistors provided in terminated lines, which are respectively connected to other ends of the plurality of third diodes.

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4. A high frequency switch according to claim 3, further comprising capacitors connected to the ends of the first to third diodes in order to make ON-OFF control of the diodes and DC bias circuits connected thereto in order to apply a DC

20 bias.